2181℔

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Keith Dow   Art Unit : 2181
Serial No.: 09/461,643   Examiner : Christopher E. Lee
Filed   : December 14, 1999
Title   : IMPROVED SIGNAL ROUTING BETWEEN A MEMORY CONTROL
           UNIT AND A MEMORY DEVICE

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Commissioner for Patents
Washington, D.C. 20231

TRANSMITTAL LETTER

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Correspondence relating to this application is enclosed.
The required fees are computed below. Please apply any charges
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Total					
Claims	22	-	20	=	2	$36
Independent	4	-	4	=	0	$0

TOTAL FEE DUE $36

A check for $36 is attached.

Respectfully submitted,

Date: 8/7/02

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RESPONSE

In response to the action mailed May 9, 2002, please amend the application as follows:

In the specification:

Paragraph beginning at page 1, line 10 has been amended as follows:

-- FIG. 1 shows a typical computer system 100. The computer 100 includes a central processing unit (CPU) 105, or processor, and a memory repeater hub 110 ("memory unit" 110). A memory control unit (MCU) 120 controls the flow of data into and out of the memory unit 110. The memory unit 110 always includes volatile memory, such as dynamic random access memory (DRAM). The computer also includes other system components, including a non-volatile storage device, such as a hard disk 125, and a modem 130 to connect the computer 100 to a network 135. A bus 115 connects the components 105, 120, 125 and 130 of computer

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